## General Description

The MAX40007 evaluation kit (EV kit) is a fully assembled and tested circuit board that contains all the components necessary to evaluate the MAX40007 IC, offered in a space-saving $1.1 \mathrm{~mm} \times 0.76 \mathrm{~mm}$, 6-bump wafer-level package (WLP). The device is a rail-to-rail micropower op amp drawing only 700nA of supply current. The EV kit operates from a single 1.7 V to 5.5 V DC power supply.

## Features

- 1.7 V to 5.5 V Single-Supply Operation
- Comes in Unity-Gain Buffer Configuration
- Can Be Configured in Inverting, Non-Inverting, and Differential Amplifier Configurations
- Evaluates the Device in a 6-Bump WLP
- Proven PCB Layout
- Fully Assembled and Tested


## Quick Start

## Required Equipment

- MAX40007 EV kit
- 1.7 V to $5.5 \mathrm{~V}, 100 \mathrm{~mA}$ DC power supply
- Voltmeter


## Ordering Information appears at end of data sheet.

## Procedure

The EV kit is fully assembled and tested. Follow the steps below to verify board operation. Caution: Do not turn on power supplies until all connections are completed and turn on $\mathrm{V}_{\mathrm{CC}}$, $\mathrm{V}_{\mathrm{SS}}$ supplies before turning on power supplies on the input pins.

1) Make sure J 1 jumper is uninstalled and J2 jumper is in 2-3 position for single-supply operation. J2 should be in 1-2 position for split-supply operation.
2) Single-supply operation: Connect the positive terminal of the +5 V supply to the VDD test point and the GND terminal of supply to the GND test point. Make sure J 2 is in 2-3 position. The power supply should be off.
3) Connect the positive terminal of the precision voltage source to the IN+ test point.
4) Connect the DMM to monitor the voltage on the OUT test point.
5) Turn on the 5 V power supply and apply 2.5 V from the precision voltage source. Observe the output at the OUT test point on the DMM. OUT should read approximately 2.5 V . Also, vary IN+ voltage between 0.05 V to 3.9 V to see if DMM on the OUT test point follows the IN+ voltage applied.
6) Split-supply operation: Connect the positive terminal of the +2.5 V supply to the VDD test point and the GND terminal of the supply to the GND test point. Connect -2.5 V supply to VSS test point. Make sure J2 is in 1-2 position for this test.
7) Connect the positive terminal of the precision voltage source to the IN+ test point.
8) Connect the DMM to monitor the voltage on the OUT test point.
9) Turn on the +2.5 V and -2.5 V power supply and apply 1 V from the precision voltage source. Observe the output at the OUT test point on the DMM. OUT should read approximately 1 V . Also, vary IN+ voltage between -2.45 V to 1.4 V to see if DMM on the OUT test point follows the applied $\mathrm{IN}+$ voltage.

## Detailed Description of Hardware

The MAX40007 EV kit contains the MAX40007 IC, which is a rail-to-rail output micropower op amp with an ultra-low 700nA supply current designed in a 6-bump WLP. The EV kit operates from a single 1.7 V to 5.5 V DC power supply.

## Default Application Circuit

The EV kit comes preconfigured in a unity-gain buffer configuration.

## Op Amp Configurations

The EV kit provides flexibility to easily reconfigure the op amp into any of the three common circuit topologies: inverting amplifier, noninverting amplifier, differential amplifier. These configurations are described in the next few sections.

## Noninverting Amplifier

To configure the device as a noninverting amplifier, replace R4 and R3 with suitable resistors. Install J1 to configure the op amp into noninverting mode. The output voltage (VOUT) for the noninverting configuration is given by the following equation:

$$
\mathrm{V}_{\mathrm{OUT}}=\left(1+\frac{\mathrm{R} 4}{\mathrm{R} 3}\right)\left(\mathrm{V}_{\mathrm{IN}+}+\mathrm{V}_{\mathrm{OS}}\right)
$$

where:
$\mathrm{V}_{\mathrm{OS}}=$ Input-referred offset voltage.
$\mathrm{V}_{\mathrm{IN}+}=$ Input voltage applied at the IN+ PCB pad.

## Inverting Amplifier

To configure the device as an inverting amplifier, replace R4 and R3 with suitable gain resistors. An appropriate DC voltage ( $V_{D C}$ ) should be applied to the $I N+$ test point to level-shift the output voltage of the op amp if the applied input voltage ( $\mathrm{V}_{\mathrm{IN}_{-}}$) at the IN - test point pad is positive:

$$
\mathrm{V}_{\mathrm{OUT}}=-\frac{\mathrm{R} 4}{\mathrm{R} 3}\left(\mathrm{~V}_{\mathrm{IN}-}\right)+\left(1+\frac{\mathrm{R} 4}{\mathrm{R} 3}\right)\left(\mathrm{V}_{\mathrm{OS}}\right)+\left(\mathrm{V}_{\mathrm{DC}}\right)
$$

## Differential Amplifier

To configure the device as a differential amplifier, replace $R 2, R_{C l}, R 3$, and $R 4$ with appropriate resistors. When $R_{C l}$ $=R 4$ and R2 = R3, the CMRR of the differential amplifier is determined by the matching of ratios $R 3 / R 4$ and $R 2 / R_{\mathrm{Cl}}$ :

$$
\mathrm{V}_{\mathrm{OUT}}=\operatorname{GAIN}\left(\mathrm{V}_{\mathrm{IN}+}-\mathrm{V}_{\mathrm{IN}-}\right)+\left(1+\frac{\mathrm{R} 4}{\mathrm{R} 3}\right) \mathrm{V}_{\mathrm{OS}}
$$

where:

$$
\mathrm{GAIN}=\frac{\mathrm{R}_{\mathrm{Cl}}}{\mathrm{R} 2}=\frac{\mathrm{R} 4}{\mathrm{R} 3}
$$

*Note: $\mathrm{R}_{\mathrm{CI}}$ means resistor on CI Pad.

## Buffer Amplifier

By default, the EV kit is configured as a standard unitygain buffer.

$$
\mathrm{V}_{\mathrm{OUT}}=\mathrm{V}_{\text {IN+ }+}+\mathrm{V}_{\mathrm{OS}}
$$

## Table 1. Default Jumper Settings

| JUMPER | SHUNT POSITION | DESCRIPTION |
| :---: | :---: | :--- |
| JU1 | Not installed | IN- to GND |
|  | $2-3$ | V $_{\text {SS }}$ = GND |
|  | $1-2$ | User-defined $V_{\text {SS }}$ on VSS test point |

## Component Suppliers

| SUPPLIER | WEBSITE |
| :---: | :---: |
| Murata Electronics North America, Inc. | www.murata.com |

Note: Indicate that you are using the MAX40007 when contacting this component supplier.

## Ordering Information

| PART | TYPE |
| :---: | :---: |
| MAX40007EVKIT\# | EV Kit |

\#RoHS-compliant
MAX40007 EV Kit Bill of Materials

| mem | arr | REF DES | Var Status | Maxinv | mFg Part\# | MANUFACTURER | Value | DEscription | COMMENTS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | 2 | с3, 65 | Pref | 20.00011-BA63 | GCJ188R71H104KA12 GCM188R71H104K; CGA3E2X7R1H104K080AE | MURATA; Tok | 0.14 F | CAPACITOR; SMT (0603); CERAMIC CHIP; 0.1 $1 \mu$ F; 50 V ; <br> TOL $=10 \% ;$ TG $=-55^{\circ} \mathrm{C}$ TO $+125^{\circ} \mathrm{C} ; \mathrm{TC}=\mathrm{X7R}$; AUTO |  |
| 2 | 3 | GND, GND_1 GND_2 | Prof | 02-TPM $15001-00$ | 5011 | KEystone | NA | TEST POINT; PIN DIA $=0.1251 \mathrm{~N} ;$ TOTAL LENGTH $=0.4451 \mathrm{~N} ;$ BOARD HOLE $=0.063 \mathrm{IN}$ BLACK; PHOSPHOR BRONZE WIRE SILVER PLATE FINISH RECOMMENDED FOR BOARD THICKNESS=0.062IN; NOT FOR COLD TEST |  |
| 3 | ${ }^{3}$ | $\mathbb{N}^{+}+$, $\mathbb{N}$, vout | Pref | 02-TPM115012-00 | ${ }^{5012}$ | KEystone | NA | TEST POINT; PIN DIA $=0.1251 \mathrm{~N} ;$ TOTAL LENGTH $=0.4451 \mathrm{~N} ;$ BOARD HOLE $=0.063 \mathrm{IN}$ WHITE; PHOSPHOR BRONZE WIRE SILVER PLATE FINISH; RECOMMENDED FOR BOARD THICKNESS $=0.062$ IN; NOT FOR COLD TEST |  |
| 4 | 1 | ${ }^{1}$ | Pref | 01.PECO2SANTVP-21 | PECO2SAAN | SuLINS | PECO2SAAN |  |  |
| 5 | 1 | ${ }^{2}$ | Preer | 01-PECO3SAAN3P-21 | PECO3SAAN | SuLINs | PECO3SAAN | CONNECTOR: MALE: THROUGH HOLE: BREAAKAWAY : STRAIGHT; ЗPINS |  |
| 6 | 4 | R1, R2, R4, R5 | Pref | 80.0000R-27 | CRCW06030000ZS MCR03EZPJJ000; ERJ-3GEYOR00 | VISHAY PALEROHMM PANASONIC | - | RESIITOR; 0603; 0n; \%\%; JUMPER; 0. 10 : THICK FLM |  |
| 7 | 2 | SU1, SU2 | Prof | 02-JMPFSTC022YAN-00 | stcoosyan | SULIINS ELECTRONICS CORP. | stco2SYAN | TEST POINT; JUMPER; STR; TOTAL LENGTH $=0.256 \mathrm{IN}$; BLACK; INSULATION $=$ PBT CONTACT $=$ PHOSPHOR BRONZE; COPPER PLATED TIN OVERALL |  |
| 8 | 1 | U1 | Pref | Max40007 | max40007 | maxim | maxa0007 | EVKIT PART-IC MAX40007EVKIT\#: OZ26 <br> PACKAGE OUTLINE: 21-100086C; PACKAGE CODE: N60D1+1; WLP6 |  |
| 9 | 1 | vDD | Pref | 02-TPMIN55000.00 | 5010 | KEYSTONE | NA | TESTPOINT WTH 1.80MM HOLE DAA, RED, MULTPUURPOSE; NOT FOR COLD TEST |  |
| 10 | 1 | vss | Prot | 02-TPM115013-00 | 5013 | KEvstone | NA | TEST POINT; PIN DIA $=0.125 \mathrm{IN} ;$ TOTAL LENGTH $=0.4451 \mathrm{~N} ;$ BOARD HOLE $=0.063 \mathrm{IN}$ ORANGE; PHOSPHOR BRONZE WIRE SILVER PLATE FINISH: RECOMMENDED FOR BOARD THICKNESS $=0.062$ IN ; NOT FOR COLD TEST |  |
| ${ }^{11}$ | 1 |  | Pret | EPCB40007 | MAX40007 | MAXIM | PCB | PCB: MAX40007 |  |
| total | 20 |  |  |  |  |  |  |  |  |
| DO NOT PURC HASE (ONP) |  |  |  |  |  |  |  |  |  |
| пем | arr | REF DES | Var Status | Maxinv | MFG PART\# | MANUFACTURER | Value | DESCRIPTTION | ComMENTS |
| 1 | 4 | C1, C2, C4, C6 | DNP | NA | N/ | N/A | OPEN | PACKAGE OUTLINE OO63 NON.PPLAR CAPACITOR- EVKIT |  |
| 2 | 2 | R3, R6 | DNP | NA | N/A | N/A | OPEN | PACKAGE OUTLINE 0063 RESIITOR-EVKIT |  |
| Total | 6 |  |  |  |  |  |  |  |  |
| PACKOUT (These are purchased part but not assembled on PCB and will bos shipeed with PCB) |  |  |  |  |  |  |  |  |  |
| пем | arr | REF DES | Maxinv | MFG PART\# | MANUFACTURER | value | DESCRIPTION | COMMENTS |  |
| 1 | 1 | PACKOUT | ${ }^{88.00711-S M L}$ | ${ }^{88.00711-S M L}$ | N/ | ? | BOX:SMALL BROWN <br> 3/16X7X1 1/4-PACKOUT |  |  |
| 2 | 1 | PACKOUT | 87-02162.00 | ${ }^{877.02162 .00}$ | NA | ? | ESD BAG;BAG;STATIC SHIELD ZIP 4inX6in; WIESD LOGO - PACKOUT |  |  |
| 3 | 1 | PACKOUT | 85-MAXKTT-PNK | ${ }^{\text {85-MAXKIT-PNK }}$ | NA | ? |  |  |  |
| 4 | 1 | PACKOUT | EVINSERT | Evinsert | N/ | ? | WEB INSTRUCTIONS FOR MAXIM DATA SHEET |  |  |
| total | 5 | PACKOUT | ${ }_{85-44003.006}$ | 85.84003006 | NA | ? | LABELLEV KIT BoX) - PACKOUT |  |  |
| Total | 5 |  |  |  |  |  |  |  |  |

## MAX40007 EV Kit Schematic



## MAX40007 EV Kit PCB Layout Diagrams



MAX40007 EV Kit—Top Silkscreen


MAX40007 EV Kit—Top Paste


MAX40007 EV Kit—Top Mask


MAX40007 EV Kit—Top

## MAX40007 EV Kit PCB Layout Diagrams (continued)



MAX40007 EV Kit—Bottom


MAX40007 EV Kit—Bottom Mask


MAX40007 EV Kit—Bottom Silkscreen

## Revision History

| REVISION <br> NUMBER | REVISION <br> DATE | DESCRIPTION | PAGES <br> CHANGED |
| :---: | :---: | :--- | :---: |
| 0 | $1 / 17$ | Initial release | - |

